

```
case_new(mon_thread, 5000, in_ports[1]),
while (terminate==0) {
chan_in_message(bytes, f, in_ports[0]);
if (terminate) break;
graph3L_display(G, f);
an_in_message(32, c, in_ports[1]);
graph3L_set_title(G, c);
chan_in_message(32, c, in_ports[1]);
chan_in_message(32, u, in_ports[1]);
graph3L_set_x(G, c, u);
chan_in_message(32, c, in_ports[1]);
chan_in_message(32, u, in_ports[1]);
graph3L_set_y(G, c, u);
for(double, dscale, in_ports[1]);
of(0), dscale(1);
of(double), dscale, in_ports[1]);
own flag when
in_ports[1]);
in_ports[1]);
in_ports[1]);
in_ports[1]);
in_ports[1]);
```

The Multiprocessor Design Company



DIAMOND 4.0

www.3L.com



About 3L Ltd

- Founded in 1987
- Headquartered in Edinburgh, Scotland
- Provides **3L Diamond**, 'intelligent' development suite:
 - Support for Texas Instruments single-core DSPs
 - Support for Texas Instruments multi-core DSPs
 - Support for Xilinx FPGAs
- Partnerships with processor vendors
- International customer base



CMRC-NRC



SONY

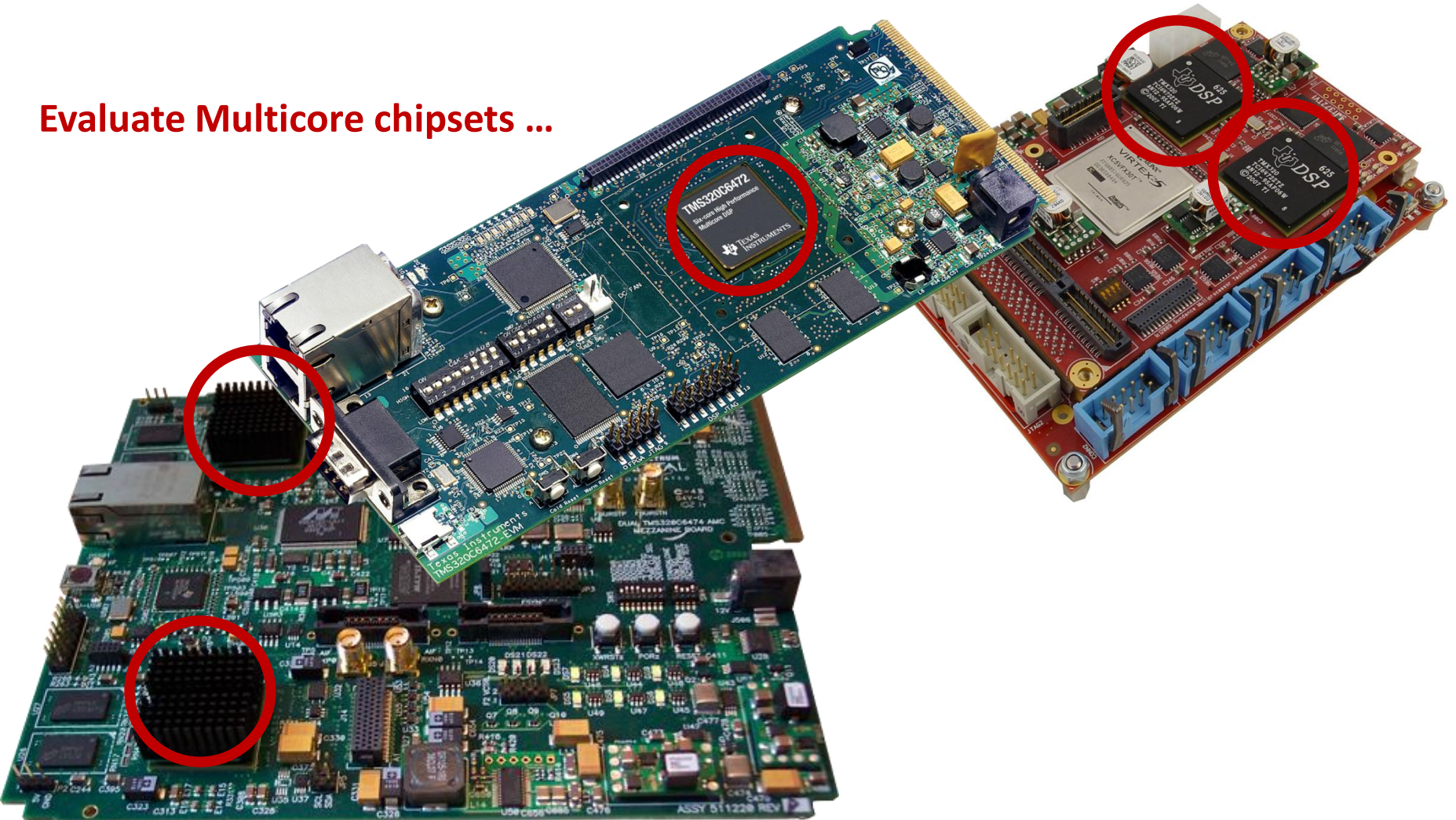


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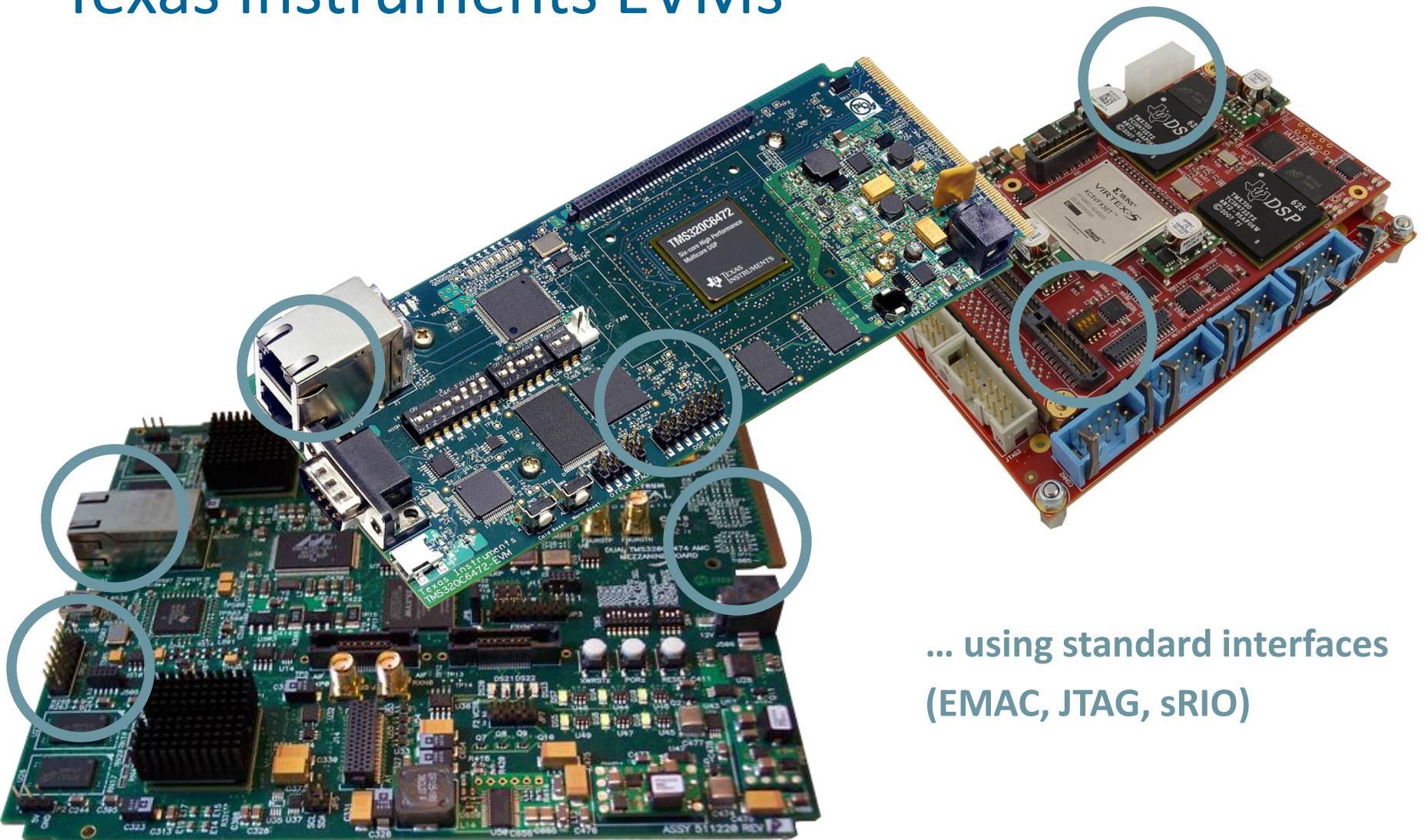


Texas Instruments EVMs

Evaluate Multicore chipsets ...



Texas Instruments EVMs



... using standard interfaces
(EMAC, JTAG, sRIO)

The Problem







How to program

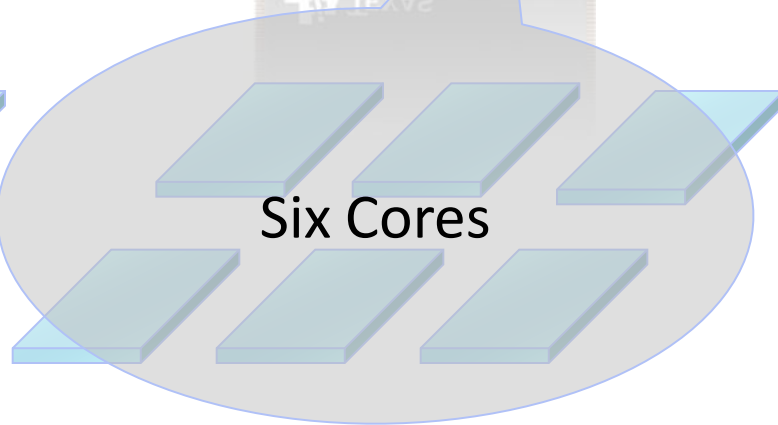
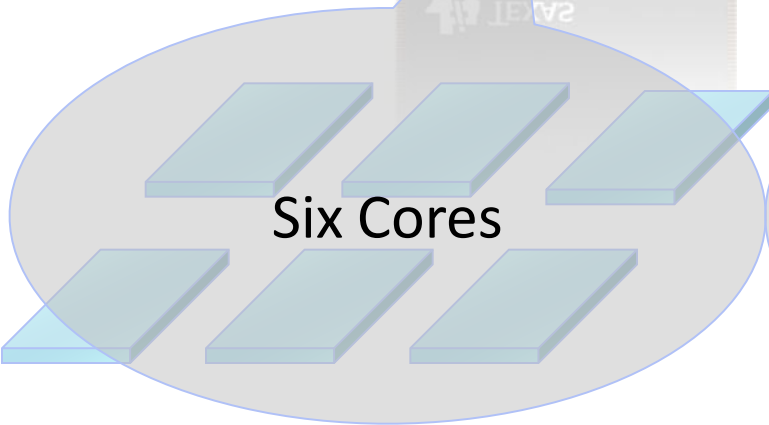
?

The Diamond Promise



-  Provide a simple, efficient model for DSP & FPGA
-  Manage complexity automatically
-  Take care of system housekeeping
-  Let you concentrate on your application

The Hardware

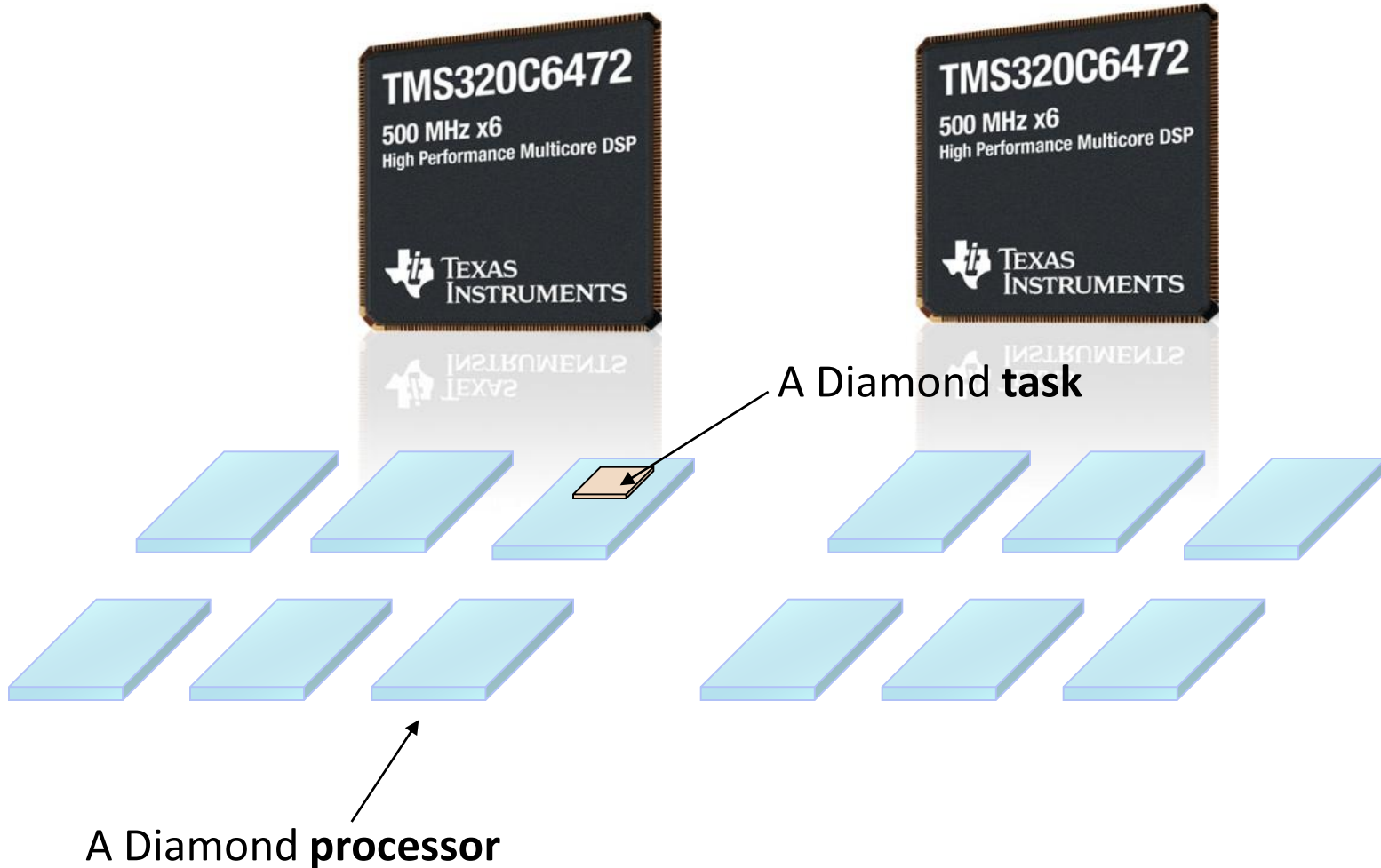


The Diamond Model

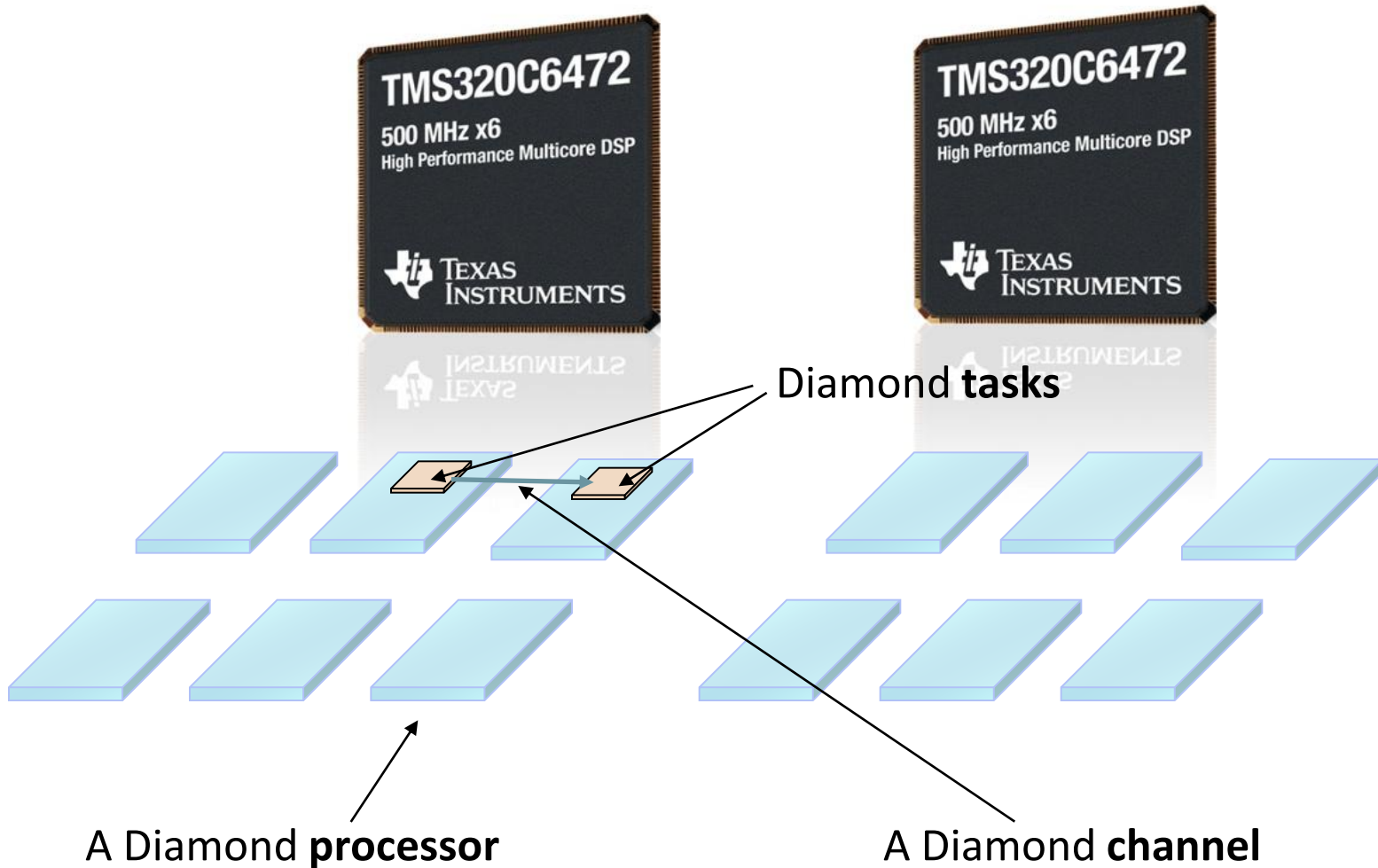


A Diamond processor

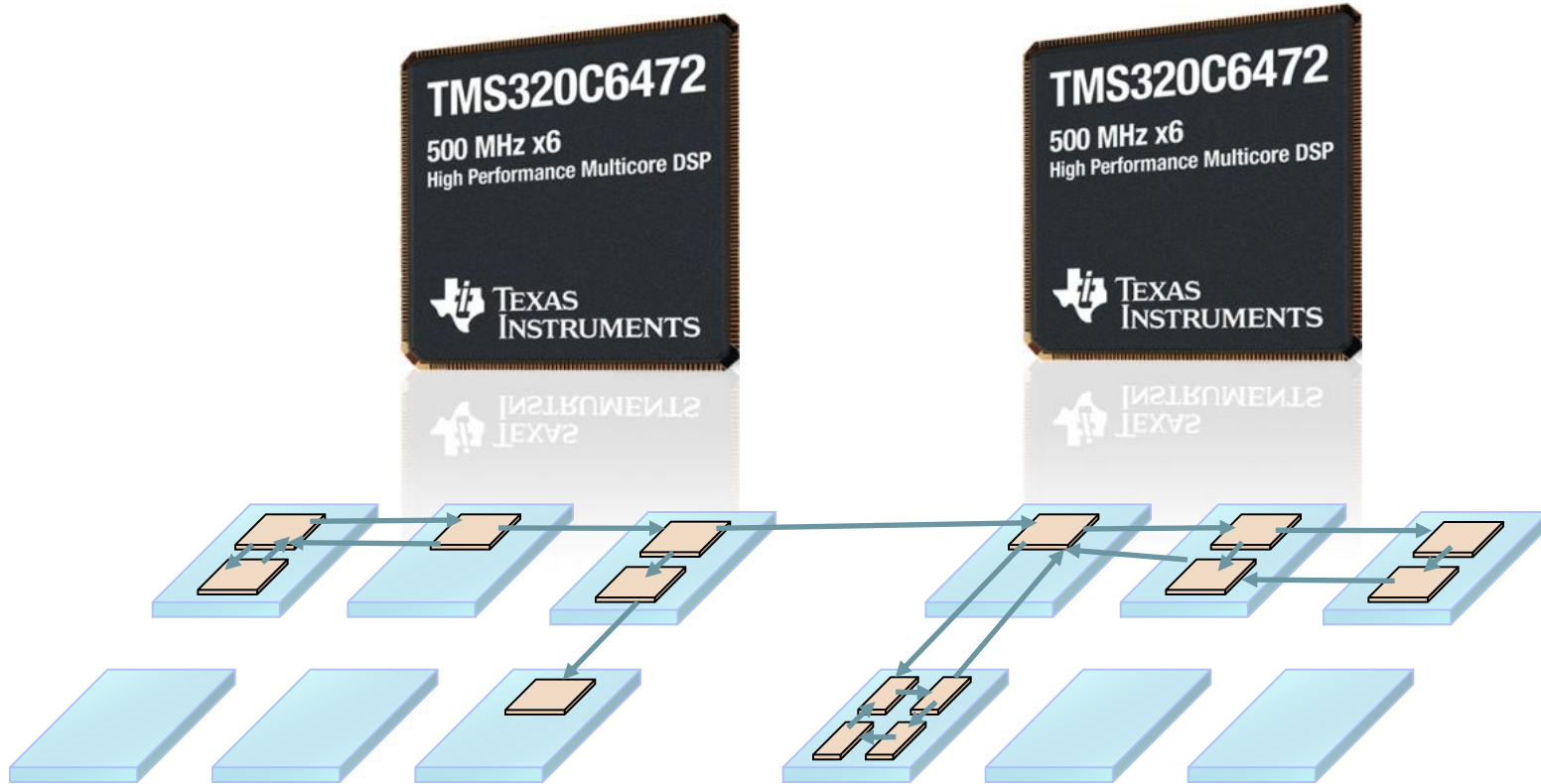
The Diamond Model



The Diamond Model

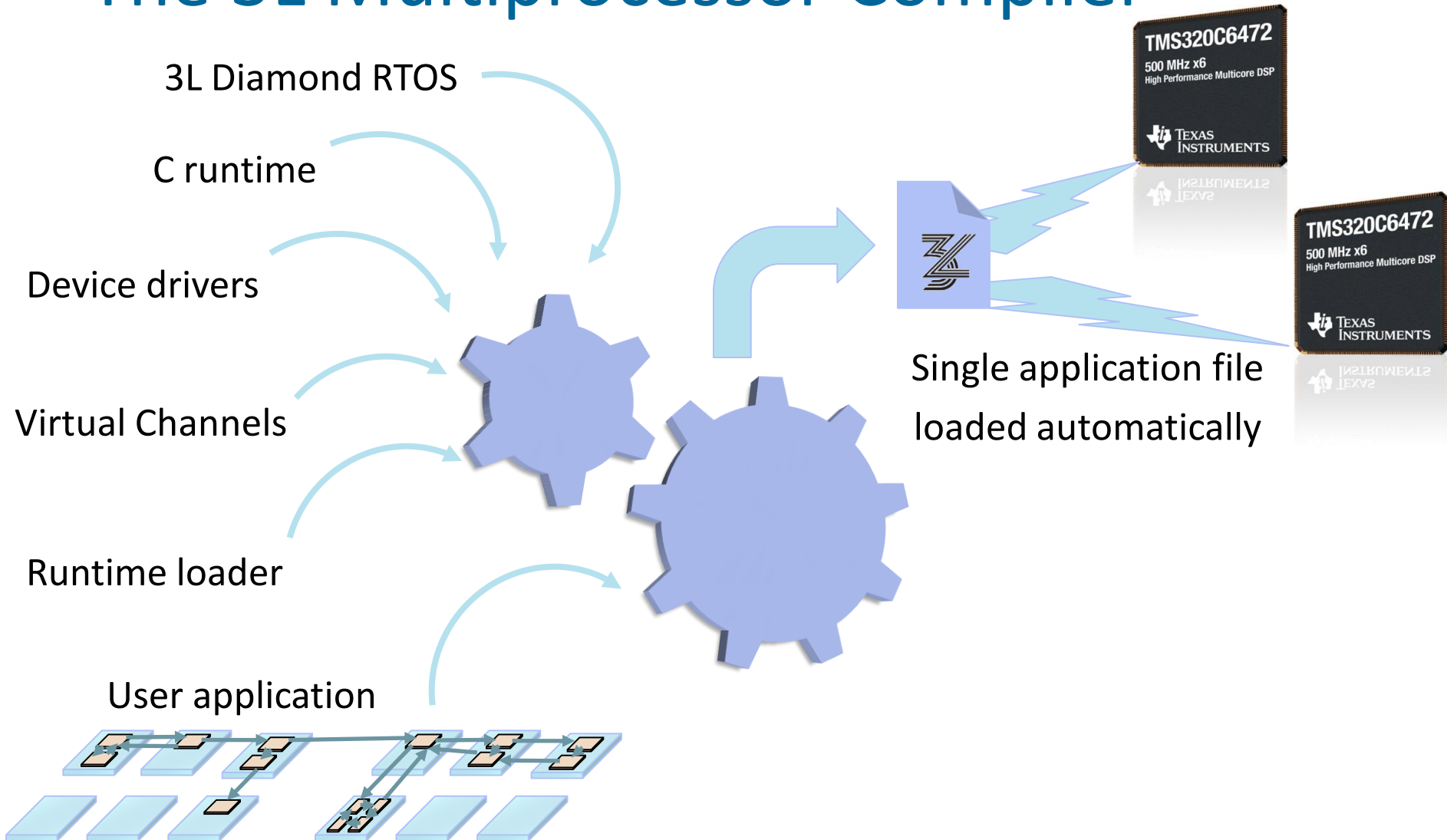


Real-life Applications

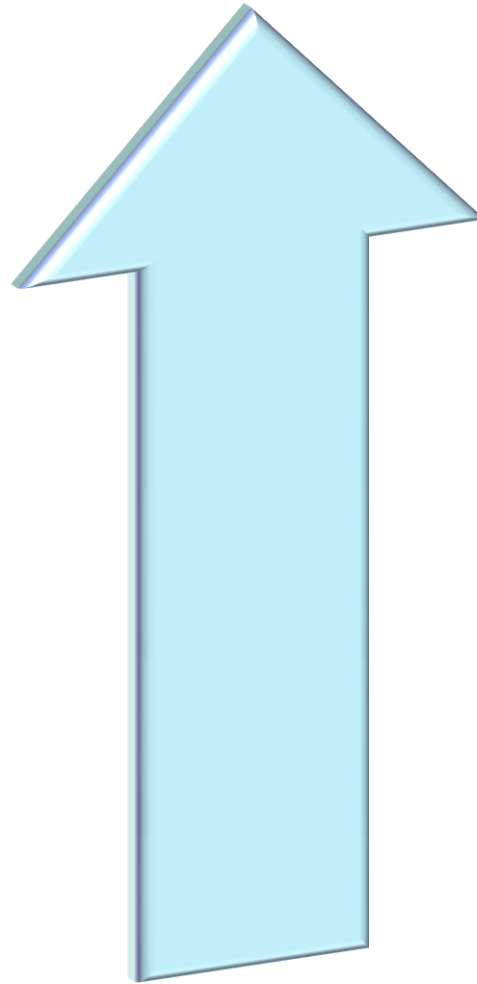


Create powerful multiprocessor applications scaling across multiple cores and chips

The 3L Multiprocessor Compiler



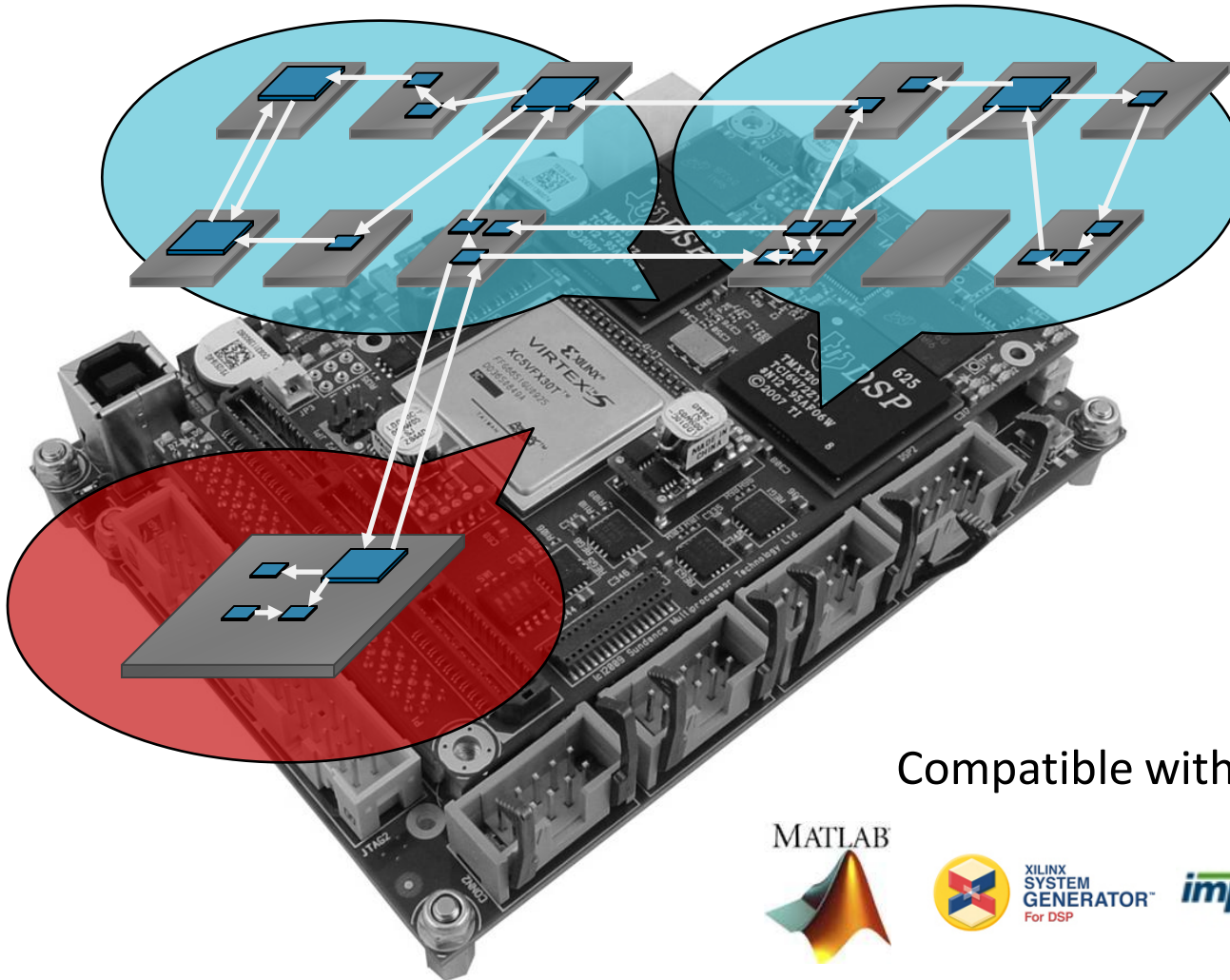
Benefits



- Housekeeping
- Time to market
- Project costs
- Unscalable code
- Project complexity

- Proven, simple model
- Easy to learn & use
- Balance between automation & control
- Optimised applications
- Standard C, VHDL & tools
- DSP & FPGA co-design
- Code reuse & scalability
- Royalty-free IP

... it also works with *FPGAs*



Requires



Compatible with



Find Out More ...

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- On-site Product Training



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