

Ittiam MP3 Encoder

MP3 Encoder

MPEG-2 MP3 audio coding (also known as MPEG-2 BC) is a coding technique used on audio signals sampled in the range of 16 kilohertz (kHz) to 48 kHz. The standard is backward compatible with the MPEG-1 MP3. It supports bit-rates from 8 to 320 kbps (depending on the sampling rate).

Features

- Encoding of MPEG-1 & 2 Layer 3 bit-streams.
- Supports bit-rates 32-320/8-160 (MPEG1/MPEG2) kbps for Layer3.
- Supports all sample rates from 16 kHz to 48 kHz.
- Mono/Stereo channels.
- Supports a simple C callable with flexible memory allocation scheme.
- Multi-channel reentrant software.
- The implementation has been tested on a variety bitstreams and audio files for robustness and quality.
- Optimized for low footprint and processing power.

Encoder Validation

MPEG MP3 Encoder is an Informative standard. There is no standard measure or tool for evaluating the quality /fidelity of the encoder. The encoders produce complex artifacts, which is dependent on the source material. Taking these into consideration, the test bench for the audio coders includes the following types of test.

Features

- Bit Stream Compliance: Tests to ensure that the generated bit-stream is in conformance with the specification.
- Objective Quality Evaluation: Audio Quality test based on the ITU BS.1387 standard for objective audio quality evaluation.
- Subjective Quality Evaluation: Listening tests to evaluate the quality.
- Artifact Listening Tests: Listening tests to ensure that the encoder does not produce the artifacts.

Resource requirements on ARM9E Processor

Function	MCPS	Pgm	Tables	Static	Scratch
	Peak	ROM (kb)		RAM (kb)	
Encode	39.6	50	13	14.62	14

Note The Data Memory mentioned in the above Table does not include Input/ Output buffers.

MCPS/MIPS indicate the CPU usage for processing Stereo/128 Kbps/44.1 KHz.

MIPS for MCPS measurement on 0 wait-state memory access

Details of ARM9E Resources required

CPU Loading

CPU Description	Simulator		Hardware	
	Ave MCPS	Peak MCPS	Ave MCPS	Peak MCPS
320 kbps, 44.1khz	41.7	47.3	55.22	65.88
192 kbps, 44.1khz	38.5	43.7	50.04	60.95
128 kbps, 44.1khz	36.0	39.6	48.05	57.45

Memory Usage

Program	Tables	Static	Scratch	Input	Output
50	13	14.62	14	4.5	8

Note:

- Memory numbers are in KB (Kilobytes)
- Performance numbers on Simulator generated with ARM RVDS Tools version 2.1. with 0-wait state memory access.
- Hardware performance generated on a ARM9E processor with 16KB of I Cache and 16 KB of D-Cache
- Hardware performance generated under Linux 2.6, using the ARM-GCC 3.4.2 Compiler
- If Packetization switch is enabled, static memory size increases by 4.23KB.
- MCPS numbers on the hardware will vary with the I-Cache and D-Cache size and with the memory configuration/place
- I/O Buffers mentioned above are for single input and output buffers



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