

G.711 Codec

Description

This data sheet contains the features supported and the performance numbers for a G.711 Encoder and Decoder on C64x_plus Platform

Features

- Encoder compresses linear PCM input signals sampled at 8 kHz to bitstream of 64 Kbps , Decoder decompresses 64 Kbps bit stream into linear PCM samples.
- Bit Compliant with ITU-T G.711 specifications
- Optimized for C64x DSP
- C callable interface for encoder and decoder
- Re-entrant multi channel implementation
- Implementation Compatible with TI XDAIS rules
- Fully interruptible Code

Performance Specification of G711 on C64x_plus

Function	MCPS		Pgm	Table	Static	Scratch
	Avg	Peak	ROM (KB)		RAM (KB)	
Encode	0.18	0.3	1.6	0.002	0.02	Nil
Decode	0.14	0.28	1.7	0.5	0.02	Nil
Codec	0.32	0.58	3.3	0.5	0.04	Nil

Peak MCPS was measured using the various test vectors and is profiled on DM6446 DSP (CCS v3.2beta)

Cache configuration used is 32Kb L1P, 16 Kb L1D and 64 Kb L2 with cache invalidation in between frames in the second case.

The MCPS have been calculated by profiling the G711Encoder and G711Decoder functions with the generated testvectors for a 10ms frame size(80 samples)

Note: All memory numbers are in bytes. Program Memory includes the XDAIS interface overhead.

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