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Data Processing News Update - October 2010

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X3-2M Multi-channel 10 MSPS PCI Express XMC Module

The X3-2M is a PCI Express XMC IO module featuring 12 simultaneously sampling 16-bit, 10 MSPS A/D channels and an FPGA processing core. It is designed for high speed instrumentation and analysis for neuro-physical, RADAR and high speed data acquisition applications.

Data acquisition control, signal processing, buffering and system interface functions are implemented in a Xilinx Spartan3 DSP FPGA, 1.8M gate device. Two 512Kx32 memory devices are used for data buffering and FPGA computing memory.



[More Information](#) ►

X3-SD16 PCI Express XMC Module with 24 bit ADCs and DACs

The X3-SD16 is a PCI Express XMC IO Module featuring Xilinx Spartan3A FPGA, 16x144KHz, 24-bit ADCs and 16x192KHz, 24-bit DAC channels and differential inputs can be programmed for 2, 10 or 20V ranges and the output is +/- 10V, DC coupled, and is excellent for vibration measurement and sonar arbitrary waveform generation.



[More Information](#) ►

X6-RX PMX/XMC Module with four 16-bit 160 MSPS A/Ds

The X6-RX is a flexible IF receiver that integrates IF digitizing with signal processing on a PMC IO module. The module provides up to 48 configurable receiver channels with a powerful Xilinx Virtex 6 FPGA signal processing core, and high performance PCI Express/PCI host interface. With the X6-RX, IF recorders can log both the digitized raw data and channels real-time sustaining rates over 2 GB/s.

The X6-RX features four, 16-bit 160 MSPS A/Ds plus an on-board digital downconverters (DDC) ASIC. IF frequencies of up to 1400 MHz are supported. The sample clock is from either a low-jitter PLL or external input. Multiple cards can be synchronized for sampling and down-conversion.

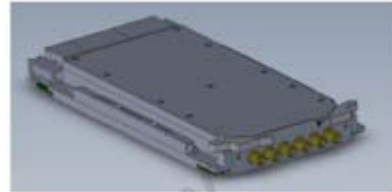


A Xilinx Virtex6 SX315T (LX240T at initial release) with 4 banks of 128MB DRAM provide a very high performance DSP core with over 2000 MACs (SX315T). The close integration of the analog IO, memory and host interface with the FPGA enables real-time signal processing at extremely high rates.

[More Information](#) ▶

XMC to VPX Adapter

The VPX-XMC module adapter allows a single width XMC module to be used in a 3U OpenVPX slot. The adapter is available in either conduction-cooled or air-cooled models.

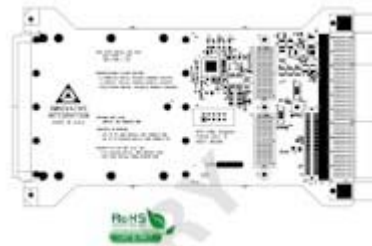


The VPX-XMC allows creation of rugged VPX systems when used with the new VPX controller from Innovative Integration.

[More Information](#) ▶

VPX Coprocessor VPX6-COP

The VPX coprocessor allows creation of rugged VPX embedded systems without requiring the complexity of typical system controllers which use ARM, PPC or Intel CPUs and associated chipsets. This controller boots from flash, consumes < 10W and all code runs entirely within the Virtex 6.



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