New Altera FPGA Product News from KCL

GiDEL's new Altera Stratix IV boards now available from KCL.

Main products are:

- PROCeIV™ - PCIe x 4 Stratix IV Computational Accelerator
- PROC104™ - Industrial PCIe/104 Stratix IV FPGA Board
- PROCStar IV™ - PCIe x 8 Stratix IV Computational Accelerator
- PROC Developers Kits
- C Programming support for GiDEL hardware
- High-Performance Reconfigurable Computer at Florida University

PROCeIV™ - PCIe x 4 Stratix IV Computational Accelerator

The PROCe IV™ system provides a high-capacity, high-speed Stratix IV FPGA-based platform, resulting in a powerful and highly flexible system. The PROCe IV™ can be hosted via 4-lane PCI Express. The board's high speed performance coupled with memory and add-on daughter boards' flexible architecture enable the system to meet almost any computational needs. In addition to 512MB onboard memory, two SODIMM sockets provide up to 8GB of memory or additional connectivity and logic.

More Information

PROC104™ - Industrial PCIe/104 Stratix IV FPGA Board

The PROC104™ system provides a high-capacity, high-speed Stratix IV FPGA-based platform fortified with high throughput and massive memory, resulting in a powerful and highly flexible system.

The PROC104™ complies with PCIe/104™ standard incorporating compact, self-stacking and rugged industrial-standard connectors. This powerful platform is ideal for high performance FPGA development and deployment across a range of SWaP-constrained application areas, including signal intelligence, image processing, software defined radio and autonomous modules/vehicles.

More Information

PROCStar IV™ - PCIe x 8 Stratix IV Computational Accelerator

The PROCStar™ offers upto 4 Stratix IV FPGAs, 2GB onboard memory, 8 SODIMM sockets (upto 32GB of memory) and

supports 5 PROCStar IV™ daughter cards.

The PROCStar IV™ System, with GiDEL's PROCDeveloper's Kit and tools, offers an incredible performance yet supports quick implementation of your unique design. Abundant memory conjoined with fast PCIe connection enable strong co-processing between a standard PC operating system and the FPGA acceleration.

More Information ▶

PROC Developers Kits

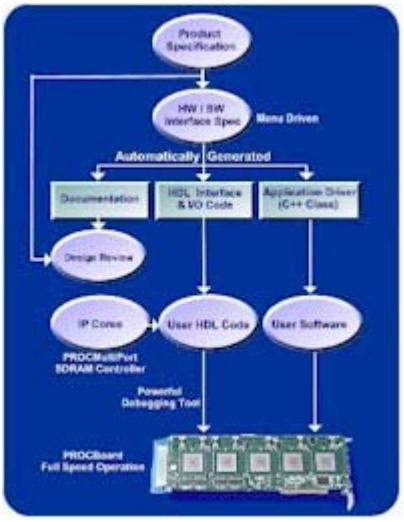
The PROCDeveloper's Kit, GiDEL's intuitive design and debug environment, facilitates design development effort on the PROCStar IV system. The kit contains PROCWizard™, PROCMultiPort™ IPs, Quartus and USBBlaster, and a PROC_HILS™ option.

The PROCWizard™ performs hardware initialisation and automatically generates interface documentation, C++ class (es) application driver(s), top-level designs, interface modules / entities and on-board memory controllers.

The PROCMultiPort™ core IPs provides simple access as FIFOs and frame delays to the on-board DRAM.

The USBBlaster provides visibility to internal signals using the available FPGA memory.

The PROC_HILs™ enables developers to accelerate Simulink™ design simulations from within the Simulink environment by implementing Hardware-in-the-Loop simulation on the PROCe IV board. Alternatively, the PROC_HILS™ may be used, via Simulink as a design entry tool for an FPGA based accelerator.



More Information ▶

C Programming support for GiDEL hardware

Support for C Programming of the Altera FPGAs on GiDEL cards is provided by Impulse C™ a C Language development tool for FPGAs.

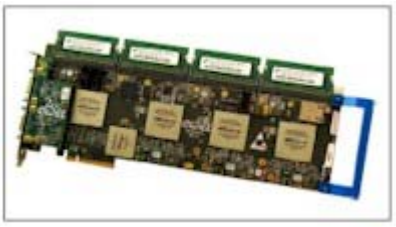
Impulse C™ provides software-to-FPGA solutions for embedded and high performance computing. Impulse C™ is used by 8 of the top 10 government contractors, half the worldwide automotive manufacturers and by a wide range of medical, industrial and consumer processing designers.

impulse
accelerated technologies

More Information ▶

High-Performance Reconfigurable Computer at Florida University

Altera, GiDEL and Impulse are supporting Florida's Novo-G, being built at CHREC, the NSF Center for High-Performance Reconfigurable Computing. Novo-G will be the most powerful Reconfigurable Computing (RC) machine ever fielded for research. It links 24 PCI Express PROCStar III GiDEL Boards, equipped with 96 top-end Altera Stratix III FPGAs and 408GB of memory. These boards are hosted in 24 servers with 576GB of memory and 20 GB/s InfiniBand.



More Information ▶

This eNews is published by Kane Computing Ltd, distributors of DSP/FPGA, Broadcast, Image Processing, Machine Vision, Audio/Video Compression and Telecommunications Solutions.

Kane Computing Ltd respects your online time and privacy. We only send this eNews to our customers and people who have signed up to receive it, however, if you would prefer not to receive future issues of eNews, you may unsubscribe by sending an email to unsubscribe@kanecomputing.com, placing unsubscribe in the 'Subject' line.

If you have received this eNews forwarded from a colleague or friend, you may subscribe yourself by emailing sales@kanecomputing.com and placing ‘Subscribe – DSP ’ in the ‘Subject’ line.