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## New Gidel Product Announcements from KCL

Gidel continues to offer the latest cutting edge technology for high performance computing and ASIC and SoC prototyping.

- [PROCeIV](#) - Stratix IV PCI Express Card
- [PROCStarIV](#) - Multiple Stratix IV PCI Express Card
- [PSDB-HDMI](#) - HDMI/DVI Interface for PROC Cards
- [PSDB\\_LA](#) - Logic Analyser Interface
- [TotalHistory](#) - Debugger for ASIC Prototyping
- [ProcSoC-4S](#) - ASIC Prototyping Systems

### PROCeIV - Stratix IV PCI Express Card

This 4 lane PCI Express board includes a Stratix IV E360, 530 or 820 FPGA together with five level memory structure and supports two PSDB daughter boards.

In addition to 512MB onboard memory, two SODIMM sockets provide up to 8GB of memory or additional connectivity and logic. Abundant memory conjoined with fast PCIe connection enable strong co-processing between a standard PC operating system and the FPGA acceleration.



[More Information](#) ▶

### PROCStarIV - Multiple Stratix IV PCI Express Card

This 8 lane PCI Express board supports up to four Stratix IV FPGAs with sustained throughput of 18,770 GB/S to internal memory and 48 GB/S to DRAM. It supports up to five PSDB daughter boards.

The performance, memory and add-on daughter boards' flexible architecture enable the system to meet almost any computation needs. In addition to 2GB on-board memory, eight SODIMM sockets provide up to 32 GB of memory.



[More Information](#) ▶

### PSDB-HDMI - HDMI/DVI Interface for PROC Cards

This daughter board provides HDMI receivers and transmitters operating at 150MHz and 165MHz respectively and supports all HDTV formats (up to 1080p) and display resolutions of SxGA (1280 x 1024 @ 75Hz).



[More Information](#)

### PSDB\_LA - Logic Analyser Interface

This PSDB\_LA is a PSDB daughter board that provides a convenient interface between Gidel boards and an external logic analyser via Mictor and standard header connections.

[More Information](#)

### TotalHistory - Debugger for ASIC Prototyping

TotalHistory is a software only solution enabling users to define a list of signals in the design which they want to trace at full system speed. There is practically no limit on the number of signals traced. The user can then view the trace using a waveform viewer to debug and validate the design. Optionally, an API is available for queries by advanced users.

[Press Release](#)

### ProcSoC-4S - ASIC Prototyping Systems

The ProcSoC-4S are the latest rugged SoC/ASIC verification systems produced by Gidel utilising the industries largest FPGAs, Altera's Stratix IV E with 820K logic elements.

A single ProcSoC system comprises multiple configurable Proc12M™, Proc6M™ or Proc3M™ boards, each with two interconnected high speed Stratix IV 820, Stratix III 340 or Stratix II 180 FPGAs respectively. Two chassis configurations are available, ProcSoC3 and ProcSoC10, capable of supporting up to 3 or 10 Proc12M/6M/3M boards respectively. Each ProcSoC system can prototype a single SoC device or be partitioned to prototype multiple designs in parallel. The ProcSoC's unique interconnectivity topology enables any FPGA to connect directly to any other FPGA in the system even in large systems.

[Press Release](#)[More Information](#)

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