

DSP/FPGA eNews – August 2007

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D.Module2.DM642 Processing Board

The D.Module2 series represents the next generation of high-performance, standalone DSP boards. These boards are optimised for highest I/O bandwidth to satisfy even more demanding applications. The 32-bit wide external bus, configured in synchronous mode, can transfer up to 533 Mbytes per second of data between DSP and peripherals.

The D.Module.DM642 includes 720MHz DM642 fixed point DSP, three 80MHz video ports, 533Mbytes per second parallel bus, 64Mbytes SDRAM, USB, ethernet and RS232 interfaces.

Visit [D.Module2.DM642](#) for further information.

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D.Module2 Tiger Shark Processing Board

The D.Module2.TS03 includes 500MHz, ADSP-TS203 TigerShark processor, 500Mbytes per second parallel bus, 1GByte link ports, 64Mbytes SDRAM, USB and RS232 interface.

Visit [D.Module2.TS03](#) for further information.

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New S.Module2 range

This is a new range of small data acquisition modules designed for use with D.Module2 processing boards.

The S.Module2.HS12-20, due out in October 2007, offers two separate Analogue inputs, each 12-bits, 20MHz sampling rate plus two 12-bit, 83KHz analogue output channels.

Visit [S.Module2.HS12-20](#) for further information.

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D.Module Data Acquisition Boards

A variety of data acquisition boards are available and cover most typical application areas: industrial control, power-line monitoring, voice processing, sonar and vibration analysis. These boards are electrically and mechanically compatible with the D.Module.DSP boards and can be

stacked with the DSP board and additional data acquisition and communication daughter cards to build a complete signal processing chain.

Visit [Comparison Chart](#) for further information

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PrgUSBP MSP430 Programming Adaptors

A new version of PrgUSBP Programming Software, which allows programming with the USBP and USBPPRO programmers, has just been released by Softbaugh, this new version offers the following additional functions:

- Selectively erase segments independently
- Programme multiple files
- Change operating voltage mid-project (USBPPRO only)
- Optionally verify files after overlays or serialisation, rather than before

This release also includes a 160 page online manual to ease transition to contract manufacturers considerably.

Email sales@kanecomputing.com for further information.

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Multi DSP/FPGA PMC

This conduction cooled PMC/XMC card offers two 1GHz, C6416 DSPs, Xilinx Virtex II VP50 FPGA, 7 rocket I/O RSL interfaces, 64-bit/66MHz PCI interface, 512MB flash disk.

Email sales@kanecomputing.com for further information.

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DM642 based programmable framegrabbers

Kane Computing have just signed a distribution agreement for the UK to sell dPict Imaging's range of Aexeon and Nexeon intelligent video imaging boards with onboard Texas Instruments DM642 DSPs and 64MB SDRAM frame buffer.

- Aexeon Bus Mastered Analogue Input Board
- Aexeon Quattro 4 Channel Video Acquisition and Compression Card
- Nexeon HD High Definition Acquisition Board for Analogue or Digital Video Inputs
- Nexeon CL Low Cost CameraLink Acquisition Board for Area or Line Scan Cameras
- nVision Multi-Format Intelligent Video Acquisition Device
- dVelooper SDK for dPict hardware

Visit [Aexeon](#) for further information.
 Visit [Aexeon Quattro](#) for further information.
 Visit [Nexeon HD](#) for further information.
 Visit [Nexeon CL](#) for further information.
 Visit [nVision](#) for further information.
 Visit [dVelooper](#) for further information.

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DaVinci SDKs supported by IgnitionPro

The ExpressDSP config kit can be used with IgnitionPro hardware. We do not supply the Linux kernel source code, only the binary file. The mv kernel has a couple of patches required to run on the IgnitionPro, these are very simple to implement and full documentation will be supplied to enable a customer to do this. The Data Visualisation Tool requires some configuration changes to the MontaVista default kernel (documented in the SDK) to work, once these changes are made and the IgnitionPro patches are applied the visualisation tool will work.

Visit [IgnitionPro](#) for further information.

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VisSim sets world record in code generation efficiency

A new version of VisSim (version 7) is soon to be released, which is a new, improved and refined easy-to-use software tool for constructing, modifying and maintaining even the most complex dynamic control system models.

VisSim was challenged to auto-generate C Code for control of 16 buck converters on the new TI F28022 and compare results with TI's own hand coded assembly routines and achieved 300KHz, the same as TI's code. VisSim took seconds to create the code. TI took over a year to create it.

Visit [VisSim](#) for further information

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Embedded Control meets Floating Point TMS320F2833x

New TMS320F2833x controllers boost performance up to 200% and reduce development time with floating point. The breakthrough series of TMS320F2833x controllers target applications that require the faster code development and performance of a floating point processor with the integration of an advanced controller. The new F2833x floating point controllers will increase performance by an average of 50% over the previous leading digital signal controllers while operating at the same 150MHz clock rate. Some DSP algorithms, such as a Fast Fourier Transform (FFT) will see a 200% improvement over an equivalent 32-bit fixed point implementation.

New F28335 eZdsp due out Q4/07.

Email sales@kanecomputing.com for further information.

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