DSP/FPGA Software News from KCL – June 2007

KCL are pleased to provide information on a number of new DSP software products and announcements.

- Optimised TCP/IP Stack for DM642 and C6455
- IntegrIT DSP Software Libraries
- WinCE for DaVinci
- MSP430 Books
- VisSim Model Based Design Software

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Optimised TCP/IP Stack for DM642 and C6455

D.SignT
Digital Signalprocessing Technology

TCP/IP object code library optimised for DM642 EVM and C6455 DSK as a DSP/BIOS task without an RTOS. The TCP/IP stack has been designed to keep code size to a minimum with low resource and memory consumption. The library also supports the D.Module2.DM642.

Visit **DS.TCP/IP**
Visit **D.Module2.DM642**

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IntegrIT DSP Software Libraries

IntegrIT
Embedding ideas...

Kane Computing Ltd has signed an agreement with IntegrIT to market their highly optimised DSP Libraries, world-wide. Libraries include NatureDSP Math Library, NatureDSP Signal library which is a collection of signal processing routines needed for implementation of typical algorithms for voice/audio processing; NatureDSP 4FSK, a software data pump for 4FSK/C4FM data transmission over UHF channels and NatureDSP Squelch+ a software package providing sub-audio signaling functions for FRS, PMR446 and analogue radios.

Visit **NatureDSP Math Library**
Visit **NatureDSP Signal Library**
Visit **NatureDSP 4FSK**
Visit **NatureDSP Squelch+**

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WinCE for DaVinci

Window CE for DaVinci SoC processors is now available through selected Advanced Software Providers (ASPs) that are supported in the UK by KCL. Using a familiar, full-featured Windows® Embedded CE operating system, designers can easily achieve video and audio performance beyond GPP capabilities.

Email **sales@kanecomputing.com** for further information.

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MSP430 Books

SoftBaugh™

SoftBaugh has released the first in a series of MSP430 books. This first book, MSP430 RF Applications with the MRF1611CC1100 and API Reference, introduces 900 MHz RF applications using the Texas Instruments MSP430F1611 mixed signal processor and CC1100 single chip low cost low power RF transceiver.

Hosted on SoftBaugh MRF1611CC1100 modules, the accompanying source code and detailed, step-by-step tutorials found in this book enable the reader to experiment with a variety of point-to-point and star network examples. For many low power sensor applications, the star topology leads to system designs capable of extended battery life, simpler application architecture, and reduced debugging effort.

Email **sales@kanecomputing.com** for further information.

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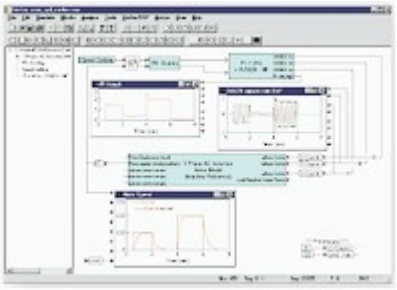
VisSim Model Based Design Software



VisSim/Embedded Controls Developer is unique in its ability to generate efficient code and target the Texas Instruments industrial control C2000 chipset, as well as the TI MSP430 low power microprocessor line. Using VisSim's intuitive graphical environment, you construct an algorithm as a block diagram of your system and VisSim will generate the most efficient code in the industry. In fact, our fixed-point block set generates code so efficient, that the resulting code can attain sample rates over 1 MHz on an F2812, and use as little as 637 bytes flash and 124 bytes RAM on an MSP430.

Email **sales@kanecomputing.com** for further information.

To download a fully featured trial version visit
www.vissim.com/downloads/demos.html.

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