Press Release

February 2010
FOR IMMEDIATE RELEASE
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New Altera Stratix IV PCI Express FPGA Boards

February 18th, 2010, Northwich, Cheshire – Kane Computing's partner, GiDEL, has released a new range of PCI Express computational accelerator boards incorporating the Altera Stratix IV family of FPGAs.

The PROCe IV™ system provides a high-capacity, high-speed FPGA-based platform fortified with high throughput and massive memory, resulting in a powerful and highly flexible system. The PROCe IV™ can be hosted via 4-lane PCI Express. The board's high speed performance coupled with memory and add-on daughter boards' enables the system to meet almost any computational need. In addition to 512MB on-board memory, two SODIMM sockets provide up to 8GB of memory or additional connectivity and logic. Abundant memory conjoined with fast PCIe connection enable strong co-processing between a standard PC operating system and the FPGA acceleration.

The PROCe IV™ supports up to 2 daughter cards to provide real-world interfaces such as CameraLink, SDI, Ethernet, etc. Applications include HPC (High Performance Computing), machine vision and imaging, high speed acquisition systems and bio-informatics.

The PROCe IV™ system, with GiDEL's PROCDeveloper's Kit and tools, offers incredible performance yet supports quick implementation of user designs.

The PROCDeveloper's Kit, GiDEL's intuitive design and debug environment, facilitates design development effort on the PROCe IV™ system. The kit contains PROCWizard™, PROCMultiPort™ and other memory control IPs, Quartus, USBBlaster and a PROC_HILs™ option.

The PROCWizard™ performs hard initialization and automatically generates; interface documentation in HTML or Microsoft Word, C++ class(es) application driver(s) enable simultaneous accesses of multiple applications, each to its dedicated section of the PROC board, top-level designs, interface modules/entities and on-board memory controllers for application use and device constraints (as pin-outs).

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The PROCMultiPort™ and other memory control IPs provides simple access as FIFOs and frame delays to the on-board DRAM. It enables parallel access to the on-board memory while enabling to split the physical memory into multiple logical memories, which simplifies design and enhances system performance and replaces the need for inventory of special memories by using standard memory and IP.

The USBBlaster™ enables the users to design in Simulink™ while accelerating enormously the design simulation on the PROCe IV™ board. Alternatively, the PROC_HILs™ may be used, via Simulink™, as a design entry tool for an FPGA based accelerator.

User designs may be in HDL, C-based, Simulink™ (graphical design) or any combination of them.

About GiDEL
GiDEL (www.gidel.com) was founded in 1993 as a high-end system development and integration company. With their project-level approach they created several powerful and advanced tools for high-performance system development. Their accumulated expertise in system level integration, together with their innovative development methodologies has proved to be valuable to companies that need to verify ASIC designs and to those companies building system-level boards. In 1997 they began providing their in-house development systems to industry. Today, GiDEL is one of the leading companies providing cost-effective integrated building blocks and production boards to system builders that need fast prototyping in order to cut development time. To ASIC / SoC/ IP designers GiDEL offers simulation acceleration, real-time emulation and verification tools.

About Altera
Altera Corporation (www.altera.com) (NASDAQ: ALTR) is the pioneer of programmable logic solutions, enabling system and semiconductor companies to rapidly and cost effectively innovate, differentiate, and win in their markets. Altera offers FPGAs, CPLDs, and ASICs in combination with software tools, intellectual property, and customer support to provide high-value programmable solutions to over 12,000 customers worldwide. Altera was founded in 1983 and has annual revenues in 2009 of US$1.20 billion. Altera is headquartered in San Jose, California, and employs approximately 2,600 people in 19 countries.

About Kane Computing
KCL (www.kanecomputing.co.uk) has been providing Image Processing, DSP and high performance computing products for use in industry, education and research since 1987 and is a Texas Instruments Third Party Partner specialising in consultancy and advice on TI development tools/platforms and image processing applications. KCL have extensive knowledge and experience of providing video compression solutions for many industries particularly for digital video security and high quality broadcast applications. KCL has a policy of continual improvement and operates its business in accordance with the requirements of ISO9001:2008.

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