



PRESS RELEASE

November, 12th 2007. For immediate release.

Multi FPGA programming is click away with PARS tool supporting HDL Coder!

RENO, NEVADA, November 26, 2007- Sundance DSP, a worldwide supplier and manufacturer of advanced Digital Signal Processing (DSP) and reconfigurable computing platforms, advanced IP cores, libraries and tools today announced the support of HDL Coder by PARS, the most powerful development tool yet designed to target parallel systems comprising DSPs and FPGAs.

Programming of multi FPGA systems is quite complex and time consuming, requiring deep knowledge of both parallel processing and FPGA programming techniques. Up to now this has always been a barrier for scientist and system level engineers to make use of heterogeneous parallel systems with many processing elements and held them back from unleashing the power of multi FPGA platforms for their applications. PARS is an established tool for developing a multi-DSP/FPGA application from a single Simulink block which is being used by prestigious customers like US Navy, General Motors, Rockwell Collins, and Oakridge National Lab for developing leading-edge applications. With the support of HDL Coder users can now focus on their application and let PARS generate the code for the FPGAs in the system using HDL Coder automatically. PARS will generate a single application from a single Simulink model which will contain the entire code of the DSPs, and FPGAs in the system plus all the communication between processors and FPGAs with built-in synchronization. HDL Coder support was provided to help Oakridge National Lab to develop a prototype for the next generation of Advanced Radar systems for US Army in record times. With PARS, ORNL engineers can use a mix of Sundance hardware platforms like SMT417 PMC module with multiple DSPs and SMT318-SX55 with multiple Xilinx Virtex-4 SX55 FPGAs and configure a tailor made hardware platform with many DSPs and FPGAs that can be programmed from a single Simulink model representing the application. All this can be achieved without any in depth understanding of the underlying hardware platforms and programming tools.

Dr. Mark Buckner, Cognitive Radio Program Director from Oak Ridge National Laboratory commented “with the combined power of PARS and Simulink we certainly have a good chance of delivering a working prototype Advanced Radar system to the Army within the shortest possible time as we do not need to develop a multi DSP/FPGA application using conventional methods of using C and VHDL programming”.

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"We are very excited to be part of the team developing tools that can let scientists and engineers focus on their application and will not be forced to spend time on learning complicated programming tools and techniques for taking their products to the market or conducting scientific research. In the next 3-12 months customer will see emergence of newer versions of PARS with much more flexibility and power and reduced cost!" said Dr. Nory Nakhaee, the C.E.O. of Sundance DSP Inc.

PARS is a Matlab/Simulink toolbox that can also generate Hardware-In-the-loop applications for real-time testing on a multiprocessor system. Target platform application can communicate with host side application being run under Simulink and exchange data back and forth through host interfaces.

Mr. Sudhir Sharma, DSP Marketing Manager from Mathworks explained that "increasingly developers turn to Mathworks tools for developing applications or conducting scientific research but need to tap in to the power of multi DSP and FPGA to satisfy the required performance. PARS is a valuable toolbox addition to many that already exist and will help our customers to reach their goals in record times".

Price of PARS is 8K dollar for a single user site and does not run-time licensing. Site license are also available.

About Sundance

Sundance DSP Inc is a US-based, independent company headquartered Chesham, U.K., and with offices in China and Italy. The company designs, develops, manufactures and markets high performance signal processing and reconfigurable systems for original equipment manufacturers in the wireless and signal processing markets. Leveraging its multiprocessor expertise and experience, Sundance provides OEM with modular DSP and FPGA-based systems as well as, Data Acquisition, I/O, Communication, and interconnectivity products that are so essential to multiprocessor systems where scalability and performance are important. With over fifty different modules and carriers for PCI, cPCI VME and Stand Alone platforms, Sundance is a solution provider to semiconductor, pharmaceutical, and factory automation industries. Sundance, founded in 1989 by the current directors, is a Xilinx Xperts and MathWorks' Connection program member. All trade mark and registered marks are acknowledged.

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