

Sundance Product Information

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SMT8121

1Gsps development platform

The SMT8121 development platform is a modular system made of multiple FPGAs and a single DSP that can be used for multi-carrier/multi-standard cellular systems, High Direct-IF infrastructures, radar systems and test equipment.

It accommodates two input channels (**8-bit resolution**) and two output channels (**14-bit resolution**) each sampling at 1GSPS.

Three Xilinx FPGAs (2x **Virtex4-SX35** and **Virtex-2 VP30**) and one TI **TMS320C6416** fixed-point DSP running at 1GHz are available for the processing of the signals. The VP30 also sports two PowerPC processing units.

SMT8121 is fully supported and supplied with the 3L Diamond design environment from **3L Ltd**. Diamond allows the user to easily and efficiently implement algorithms on any of the DSPs, FPGAs and PowerPCs available. The **SMT6025** offers a Host-side software interface to the PC.

Upgrades to more and powerful DSPs and FPGAs are available as a simple user upgrade in the field, and the SMT8121 maintains the legacy tradition of Sundance's forward and backwards compatibility.

SMT8121 is comprised of the following hardware building blocks:

- **SMT310Q**
- **SMT395-VP30**
- **SMT381** mounted on a **SMT368A**
- **SMT391** mounted on a **SMT368**
- Cable set

The software supplied is:

- **SMT6012**
- **SMT6025**
- **Diamond IDE** w. Diamond FPGA and Diamond DSP. Optional Diamond PowerPC

Power consumption figures for the SMT8121:
These figures are a guide only

+3.3V Rail : 8.4A
+5V Rail : 350mA
+12V Rail : 85mA
-12V Rail : 92mA



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